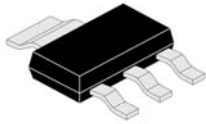


## SOT-223 Plastic-Encapsulate Transistors

*NPN Transistors*

**PZTA44**



**SOT-223**  
**Surface Mount**  
**Plastic Package**  
**RoHS compliant**

SOT-223

### Marking

ZTA44

### FEATURES:

1. Low current : 300mA(max)
2. High voltage: VCEO=400V

### ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL	VALUE	UNIT
Collector Emitter Voltage	$V_{CEO}$	400	V
Collector Base Voltage	$V_{CBO}$	400	V
Emitter Base Voltage	$V_{EBO}$	6	V
Collector Current Continuous	$I_C$	200	mA
Collector Current Pulsed	$I_{CM}$	300	mA
Collector Power Dissipation	$P_C$	1	W
Junction Temperature	$T_j$	150	°C
Storage Temperature	$T_{stg}$	-65 to +150	°C

**ELECTRICAL CHARACTERISTICS at** (Ta = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	VALUE			UNIT
			MIN	TYP	MAX	
Collector-base breakdown voltage	$V_{(BR)CBO}$	$I_C=100\mu A, I_E=0$	400	--	--	V
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	$I_C=1mA, I_B=0$	400	--	--	V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	$I_E=100\mu A, I_C=0$	6	--	--	V
Collector cut-off current	$I_{CBO}$	$V_{CB}=400V, I_E=0$	--	--	100	nA
Emitter cut-off current	$I_{EBO}$	$V_{EB}=4V, I_C=0$	--	--	100	nA
DC current gain	$h_{FE(1)}$	$V_{CE}=10V, I_C=1mA$	40	--	--	
	$h_{FE(2)}$	$V_{CE}=10V, I_C=10mA$	50	--	200	
	$h_{FE(3)}$	$V_{CE}=10V, I_C=50mA$	45	--	--	
	$h_{FE(4)}$	$V_{CE}=10V, I_C=100mA$	40	--	--	
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C=1mA, I_B=0.1mA$	--	--	0.4	V
		$I_C=10mA, I_B=1mA$	--	--	0.5	V
		$I_C=50mA, I_B=5mA$	--	--	0.75	V
Base-emitter saturation voltage	$V_{BE(sat)}$	$I_C=10mA, I_B=1mA$	--	--	0.75	V
Transition frequency	$f_T$	$V_{CE}=10V, I_C=10mA,$ $f=100MHz$	20	--	--	MHz
Collector capacitance	$C_C$	$V_{CB}=20V, I_E=0, f=1MHz$	--	--	7	pF
Emitter capacitance	$C_e$	$V_{EB}=0.5V, I_C=0, f=1MHz$	--	--	130	pF

## TYPICAL CHARACTERISTICS CURVES

Fig 1: Static Characteristics

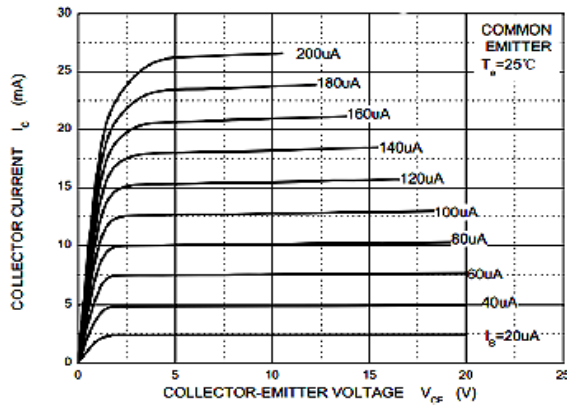


Fig 3: DC Current Gain vs Collector Current

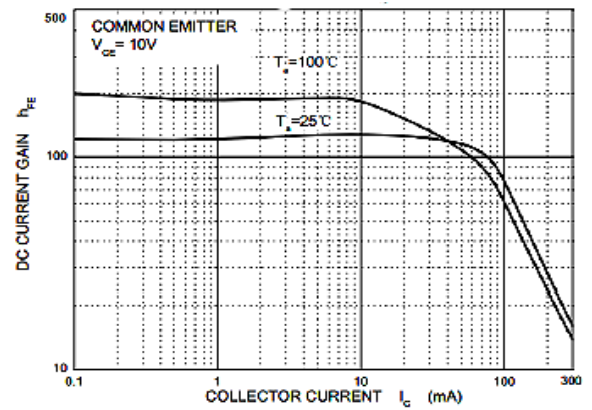


Fig 2: Base Emitter Saturation Voltage vs Current

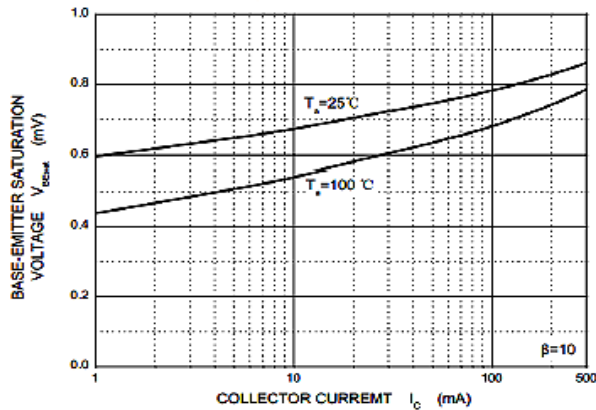
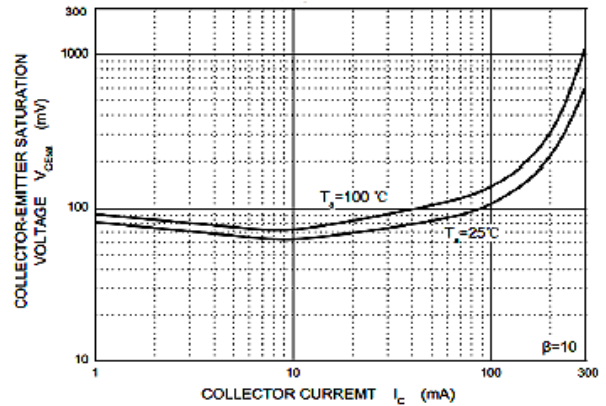


Fig 4: Collector Emitter Saturation Voltage vs Collector Current



## TYPICAL CHARACTERISTICS CURVES

Fig 5: Collector Power Dissipation vs Ambient Temperature

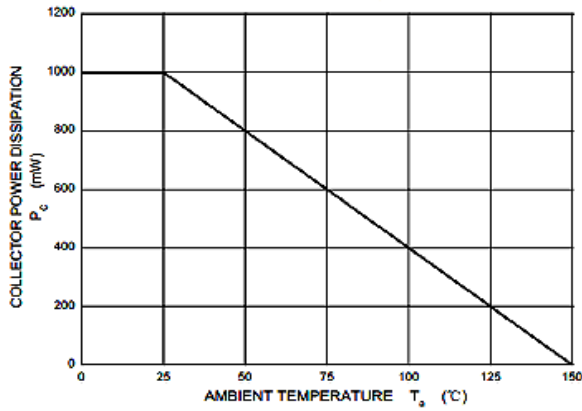


Fig 6: Capacitance vs Reverse Voltage

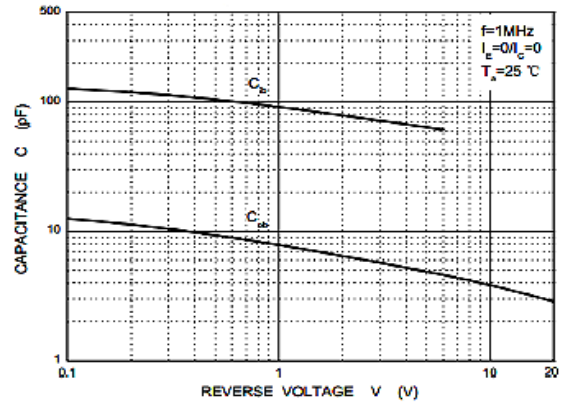
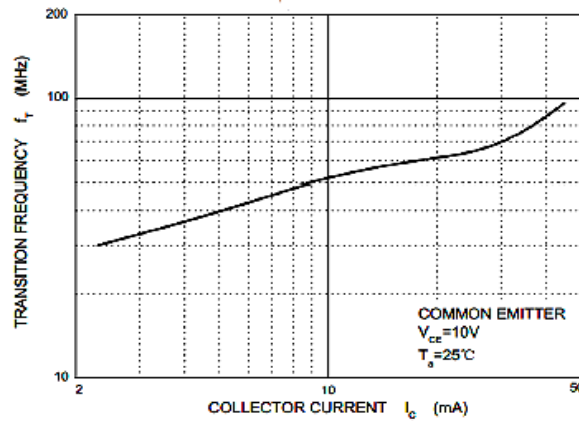
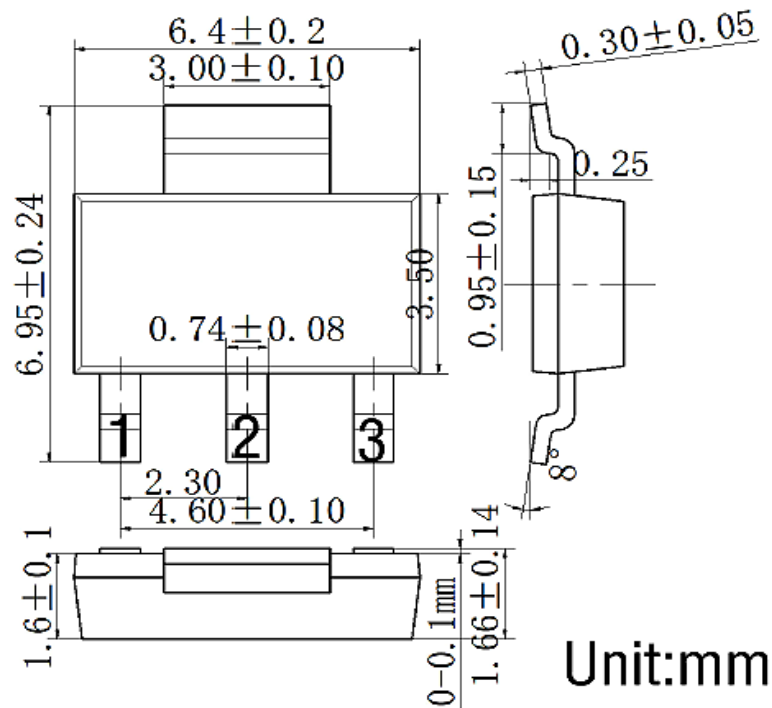


Fig 7: Transition Frequency vs Collector Current



## PACKAGE DETAILS

### SOT-223 Surface Mount Plastic Package



### PIN CONFIGURATION

1. BASE
2. COLLECTOR
3. EMITTER



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## Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

### Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

### Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level		
Level	Time	Condition
1	Unlimited	≤30 °C / 85% RH
2	1 Year	≤30 °C / 60% RH
2a	4 Weeks	≤30 °C / 60% RH
3	168 Hours	≤30 °C / 60% RH
4	72 Hours	≤30 °C / 60% RH
5	48 Hours	≤30 °C / 60% RH
5a	24 Hours	≤30 °C / 60% RH
6	Time on Label(TOL)	≤30 °C / 60% RH



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## Customer Notes

### Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

### Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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