



NPN General Purpose Transistor

PN3565

TO-92 Leaded Plastic Package RoHS compliant



DESCRIPTION:

This device is designed for use as general purpose amplifiers and switches requiring collector currents to 300 mA.

APPLICATIONS:

NPN General Purpose Amplifier and switches.

ABSOLUTE MAXIMUM RATINGS* (T_a = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	25	
Collector-Base Voltage	V _{CBO}	30	V
Emitter-Base Voltage	V_{EBO}	6.0	
Collector Current - Continuous	Ι _C	500	mA
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

THERMAL CHARACTERISTICS

Paran	Symbol	Value (Max.)	Unit	
Total Davias Dissinction	Derate at 25°C		625	mW
Total Device Dissipation	Derate above 25°C	PD	5.0	mW/°C
Thermal Resistance, Juncti	$R_{ ext{ ext{ ext{ ext{ ext{ ext{ ext{ ext$	83.3	°C/W	
Thermal Resistance, Junction to Ambient		$R_{ ext{ hetaJA}}$	200	°C/W





ELECTRICAL CHARACTERISTICS (Ta = 25°C unless otherwise specified)

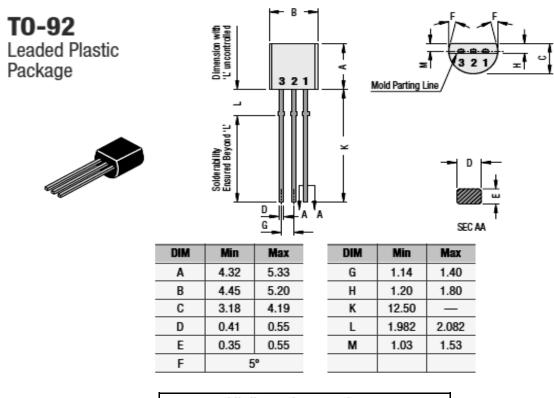
Parameter	Symbol	Test Conditions	Value		Unit	
Farameter	Symbol	Symbol Test Conditions		Max.	Unit	
OFF CHARACTERISTICS						
Collector-Emitter Breakdown Voltage*	V _{(BR)CEO}	I _C = 2.0 mA, I _B = 0	25			
Collector-Base Breakdown Voltage	V _{(BR)CBO}		30		V	
Emitter-Base Breakdown Voltage	V _{(BR)EBO}		6			
Collector Cutoff Current	I _{CBO}	V _{CB} = 25 V, I _E = 0		50	mA	
ON CHARACTERISTICS*						
DC Current Gain	h _{FE}	V_{CE} = 10 V, I _C = 1.0 mA	150	600		
Collector-Emitter Saturation Voltage	V _{CE(sat)}	$I_{\rm C}$ = 1.0 mA, $I_{\rm B}$ = 0.1 mA		0.35	V	
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	C _{ob}	V _{CB} = 5.0 V		4	pF	
	02	\$ 5				

*Pulse Test: Pulse Width \leq 300 ms, Duty Cycle \leq 2.0%





Package Details



All dimensions are in mm

Pin confugration

Pin1	Collector
Pin2	Base
Pin3	Emitter



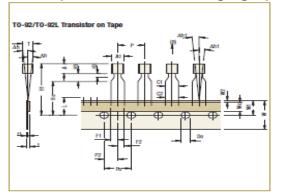


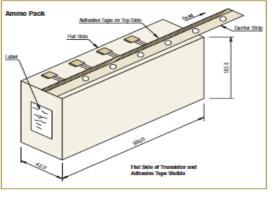
Packaging Specifications ...

T & A: Tape and Ammo Pack; **T & R:** Tape and Reel; **Bulk:** Loose in Poly Bags; **Tube:** Tube and Carton; **K:** 1,000

			Inner Carton			0	Outer Carton		
Package / Case Type	Packaging Type	Std. Packing Qty.	Qty.	Size LxW xH	Gross Weight	Qty.	Size LxW xH	Gross Weight	
		-	Nos.	(cm)	(Kg.)	Nos.	(cm)	(Kg.)	
TO-92	Bulk	1000	5K	9 x 19 x 8 1	1.1	80K	43 x 40 x 35	20.0	
10-92	T & A	2000	2K	32 x 4.5 x 20	0.7	40K	43 x 40 x 35 1	15.2	

TO-92 Tape and Ammo Packaging Specifications





Tape Specifications

Item description	Symbol
Body width	A1
Body height	A
Body thickness	Т
Pitch of component ^{CF}	Р
Feed hole pitch ^{§1}	Po
Feed hole center to	
component centre ^{§2}	P2
Comp. alignment, Side view ^{§3}	Dh
Comp. alignment, Front view ^{§3}	Dh1
Tape width ^{Cr}	W
Hold down tape width ^{Cr}	Wo
Hole position	Wi
Hold-down tape position	W2
Lead wire clinch height	Ho
Component height	Hi
Length of snipped leads	L
Feed hole diameter ^{Cr}	Do
Total tape thickness ^{§4}	t
Lead-to-lead distance ^{Cr}	F1, F2
Stand off	H2
Clinch height	H3
Lead parallelismCr	C1-C2
Pull-out force	(p)

TO-92			
Min	Nom	Max	Tol
4.45		5.20	
4.32		5.33	
3.18		4.19	
	12.7		±1.0
	12.7		±0.3
	6.35		±0.4
	0	1.0	
	0	1.3	
	18		±0.5
	6		±0.2
	9		+0.7 -0.9
0.0		0.7	
	16		±0.5
		24.0	
		11.0	
	4		±0.2
		1.2	
2.4		2.7	
0.45		1.45	
		3.0	
		0.22	
6N			

Taping Specification

- Maximum alignment deviation between leads not to be greater than 0.20 mm.
- Maximum non-cumulative variation between tape feed holes shall not exceed 1 mm in 20 pttches.
- Hold down tape not to exceed beyond the edge(s) carrier tape and there shall be no exposure of adhesive.
- No more than 3 consecutive missing components is permitted.
- A tape trailer, having at least three feed holes is required after the last component.
- Splices shall not interfere with the sprocket feed holes.
- §1 Cumulative pitch error 1.0 mm/20 pitch.
- §2 To be measured at bottom of clinch.
- §3 Attop of body. §4 t1 = 0.3 - 0.6 mm
- 94 ti = 0.3 0.6 mm Cr Critical Dimension.

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<u>Recommended Product Storage Environment for Diode and</u> <u>Transistors</u>

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying.
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years, the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start. For this the following JEDEC table may be referred:

JEDEC MSL Level				
Level	Time	Condition		
1	Unlimited	≤30 °C / 85% RH		
2	1 Year	≤30 °C / 60% RH		
2a	4 Weeks	≤30 °C / 60% RH		
3	168 Hours	<u>≤</u> 30 °C / 60% RH		
4	72 Hours	≤30 °C / 60% RH		
5	48 Hours	<u><</u> 30 °C / 60% RH		
5a	24 Hours	<u><</u> 30 °C / 60% RH		
6	Time on Label(TOL)	<u><</u> 30 °C / 60% RH		

Figure 1 Floor Life according to JEDEC MSL Level





Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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