

## Continental Device India Limited

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company





### PNP SILICON PLANAR EPITAXIAL TRANSISTORS



PN200 PN200A

TO-92 Plastic Package

# **COMPLEMENTARY PN100, PN100A**

## ABSOLUTE MAXIMUM RATINGS (T<sub>a</sub>=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	VALUE	UNITS
Collector Emitter Voltage	$V_{\sf CEO}$	35	V
Collector Base Voltage	$V_{CBO}$	60	V
Emitter Base Voltage	$V_{EBO}$	5	V
Collector Current Continuous	I <sub>C</sub>	500	mA
Power Dissipation @ T <sub>a</sub> =25°C	$P_D$	625	mW
Operating And Storage Junction Temperature Range	$T_{j},T_{stg}$	-55 to +150	°C

#### THERMAL RESISTANCE

Junction to Ambient in free air	$R_{th(i-a)}$	200	°C/W

# ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C unless otherwise specified)

DESCRIPTION	SYMBOL	TEST CONDITION	1	VALUE		
DESCRIPTION	STWIBUL	1E31 CONDITION	MIN	MAX	UNITS	
Collector Emitter Breakdown Voltage	BV <sub>CEO</sub> *	I <sub>C</sub> =1mA,I <sub>B</sub> =0	35		V	
Collector Base Breakdown Voltage	BV <sub>CBO</sub>	$I_{C}=100\mu A, I_{E}=0$	60		V	
Emitter Base Breakdown Voltage	BV <sub>EBO</sub>	I <sub>E</sub> =100μA, I <sub>C</sub> =0	5		V	
Base Cut off Current	I <sub>CBO</sub>	$V_{CB} = 35V, I_{E} = 0$		500	nA	
Collector Emitter Saturation Voltage	V <sub>CE(sat)</sub> *	I <sub>C</sub> =150mA, I <sub>B</sub> =15mA		0.4	V	
PN100, A		I <sub>C</sub> =500mA, I <sub>B</sub> =50mA		1.0	V	
PN200, A				2.0	V	
Base Emitter Saturation Voltage	V <sub>BE(sat)</sub> *	I <sub>C</sub> =150mA, I <sub>B</sub> =15mA		0.95	V	
PN100, A		I <sub>C</sub> =500mA, I <sub>B</sub> =50mA		1.2	V	
PN200, A				1.3	V	

<sup>\*</sup>Pulse Condition: = Width  $\leq$  300ms, Duty Cycle  $\leq$  2%.

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# **COMPLEMENTARY PN100, PN100A**

# ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C unless otherwise specified)

DESCRIPTION	SYMBOL	TEST CONDITION	PN100	PN100A	PN200	PN200A
DC Current Gain	h <sub>FE</sub> *	I <sub>C</sub> =1mA, V <sub>CE</sub> =1V	>40	>40	>40	>40
		I <sub>C</sub> =10mA, V <sub>CE</sub> =1V	100-450	300-600	100-450	300-600
		I <sub>C</sub> =150mA, V <sub>CE</sub> =1V*	>100	>100		
		I <sub>C</sub> =150mA, V <sub>CF</sub> =2V*			>100	>100

### **DYNAMIC CHARACTERISTICS**

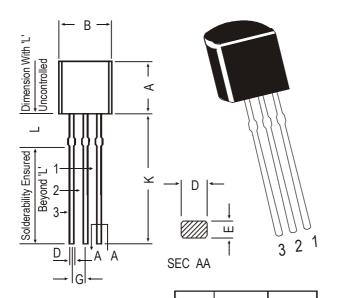
Transition Frequency	f <sub>T</sub>	I <sub>C</sub> =20mA, V <sub>CE</sub> =10V			
		f=100MHz			
PN100, A			>200		MHz
PN200, A			>150		MHz

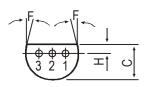
<sup>\*</sup>Pulse Condition: = Width  $\leq$  300ms, Duty Cycle  $\leq$  2%.

# **TO-92 Plastic Package**

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### **TO-92 Transistors in Tape and Ammo Pack**





PIN CONFIGURATION 1. COLLECTOR 2. BASE

3. EMITTER

DIM	MIN.	MAX.
Α	4.32	5.33
В	4.45	5.20
С	3.18	4.19
D	0.41	0.55
Е	0.35	0.50
F	5 DI	EG
G	1.14	1.40
Н	1.14	1.53

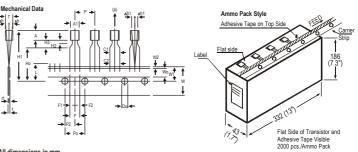
12.70

1.982

2.082

K

All diminsions in mm.



ΑII	dii	mei	ารเ	or	18	in	mn

TT-14		SPECIFICATION			ON			
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL.	REMARKS		
BODY WIDTH	A1	4.0		4.8				
BODY HEIGHT	A	4.8		5.2				
BODY THICKNESS	T	3.9		4.2				
PITCH OF COMPONENT	P		12.7		± 1.0			
FEED HOLE PITCH	Po		12.7		± 0.3	CUMULATIVE PITCH ERROR 1.0 mm/20 PITCH		
FEED HOLE CENTRE TO								
COMPONENT CENTRE	P2		6.35		± 0.4	TO BE MEASURED AT BOTTOM OF CLINCH		
DISTANCE BETWEEN OUTER					+ 0.6			
LEADS	F		5.08		- 0.2			
COMPONENT ALIGNMENT SIDE VIEW	Δh		0	1.0		AT TOP OF BODY		
COMPONENT ALIGNMENT FRONT VIEW	∆h1		0	1.3		AT TOP OF BODY		
TAPE WIDTH	W		18		± 0.5			
HOLD-DOWN TAPE WIDTH	Wo		6		± 0.2			
HOLE POSITION	W1		9		+ 0.7			
					- 0.5			
HOLD-DOWN TAPE POSITION	W2		0.5		± 0.2			
LEAD WIRE CLINCH HEIGHT	Ho		16		± 0.5			
COMPONENT HEIGHT	H1			23.25				
LENGTH OF SNIPPED LEADS	L			11.0				
FEED HOLE DIAMETER	Do		4		± 0.2			
TOTAL TAPE THICKNESS	t			1.2		t1 0.3-0.6		
LEAD - TO - LEAD DISTANCE	F1, F2		2.54		+ 0.4			
STAND OFF	H2	0.45		1.45	- 0.1			
CLINCH HEIGHT	H3			3.0				
LEAD PARALLELISM	C1 - C2			0.22				
PULL - OUT FORCE	(P)	6N						

- NOTES

  1. Maximum alignment deviation between leads will not to be greater than 0.2mm.

  2. Maximum non-cumulative variation between tape feed holes shall not exceed 1 mm in 20 pitches.

  3. Holddown tape will not exceed beyond the edge(s) of carrier tape and there shall be no exposure of adhesive.

  4. There will be no more than three (3) consecutive missing components in a tape.

  5. A laper taller, having at least three feed holes are provided after the last component in a tape.

  6. Splices should not interfere with the sprocket feed holes.

# **Packing Detail**

	PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX				
L		Details	Details Net Weight/Qty S		Qty	Size	Qty	Gr Wt		
Γ	TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs		
	TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs		

Notes PN200 PN200A

TO-92 Plastic Package

## **Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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