

An IATF 16949, ISO9001 and ISO 14001 Certified Company

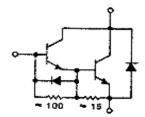




NPN SILICON POWER DARLINGTON TRANSISTOR







MJ10022 MJ10023

TO-3
Metal Can Package
RoHS compliant

FEATURES:

- 1. Continuous Collector Current I_C =40A
- 2. Switching Regulator
- 3. Inverters
- 4. Solenoid and Relay Drivers
- 5. AC and DC Motor Controls

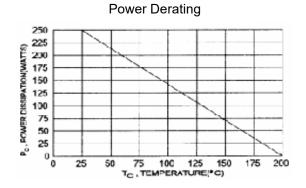
APPLICATION: General Purpose Transistor

ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C Unless otherwise specified)

PARAMETER		CVMDOL	VALUE	LINUT		
		SYMBOL	MJ10022	MJ10023	UNIT	
Collector-Emitter Voltage		V_{CEV}	450 600		V	
Collector-Emitter Voltage		$V_{CEO(SUS)}$	350	400	V	
Emitter-Base Voltage		V_{EBO}	8.0		V	
Collector Current-Continuous		I _c	40		^	
Collector Current-Peak		I _{CM}	80		Α	
Base Current		l _B	20.0		Α	
Total Power Dissipation @ T _C =25°C			250		W	
Total Power Dissipation	@ T _C =125°C	P_{D}	143		W	
Derate above 25°C			1.43		W/°C	
Operating and Storage Junction Temperature Rang		T_J,T_STG	-65 to +200		°C	

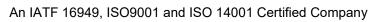
THERMAL CHARACTERISTICS

Thermal Resistance Junction to Case	$R_{\Theta ic}$	0.7	°C/W



MJ10022 MJ10023 Rev0_ 28072020E









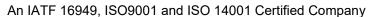
ELECTRICAL CHARACTERISTICS at (Ta = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	VALUE			UNIT
PARAMETER	STWIBOL	1EST CONDITION		TYP	MAX	UNII
Off Characteristics						
Collector-Emitter MJ10022	$V_{CEO(SUS)}$	I _C =100mA, I _B = 0				V
Sustaining Voltage MJ10023						V
		(V _{CEV} = Rated Value,V _{BE(OFF)} =1.5V)) 0.25		<u> </u>	
Collector Cutoff Current	$I_{\sf CEV}$	(V _{CEV} = Rated Value,			F 0	mΑ
		V _{BE(OFF)} =1.5V,T _C =150°C)			5.0	
Collector Cutoff Current	ı	(V _{CE} =Rated V _{CEV} ,		-	5.0	mA
Collector Cutoff Current	I _{CER}	R_{BE} =50 Ω , T_{C} =100°C)				
Emitter Cutoff Current	I _{EBO}	(V _{EB} =2.0V, I _C =0)			175	mA
On Characteristics ¹						
DC Current Gain	h _{FE}	(I _C =10A, V _{CE} =5.0V)	60	-	600	
	$V_{CE(sat)}$	(I _C =20A, I _B =1.0A)			2.2	
Collector-Emitter Saturation Voltage		(I _C =40A, I _B =5.0A)			5.0	V
		(I _C =20A, I _B =1.0A, T _C =100°C)			2.5	
Base-Emitter saturation Voltage	$V_{BE(sat)}$	(I _C =20A, I _B =1.2A)	2.5		2.5	V
base-Emitter saturation voltage		(I _C =20A, I _B =1.2A, T _C =100°C)			2.5	
Diode Forward Voltage	V_{F}	(I _F =20A)		-	5.0	V
Dynamic Characteristics						
Output Capacitance	C_ob	(V _{CB} =10V, I _E =0,f=1.0kHz)	150	-	600	pF
Switching Characteristics						
Delay Time	t_{d}	V _{CC} =250V, I _C =20A I _{B1} =1.0A,		1	0.2	us
Rise Time	t _r				1.5	us
Storage Time	t _s	$V_{BE(off)}$ =5.0V tp=50us, Duty Cycle $\leq 2\%$		-	2.5	us
Fall Time	t _f	= 2 /0			1.1	us

Notes:

1. Pulse Test Pulse width = 300 µs, Duty Cycle ≤ 2.0%









TYPICAL CHARACTERISTICS CURVES

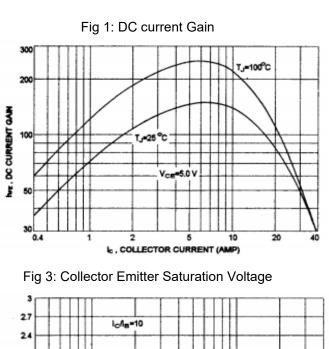


Fig 2: Collector Saturation Region

3 27 24 21 1.5 1.5 1.5 1.0 0.9 0.6 0.3 0.4 1 2 5 10 20 40 IC, COLLECTOR CURRENT (AMP)

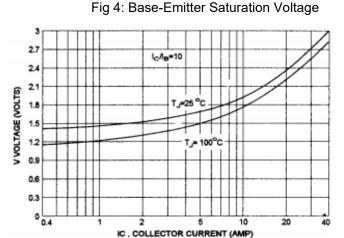


Fig 5: Collector Cut-off Region

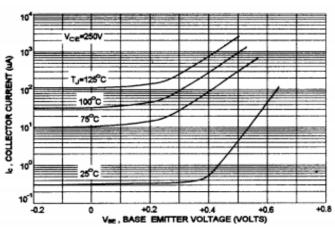
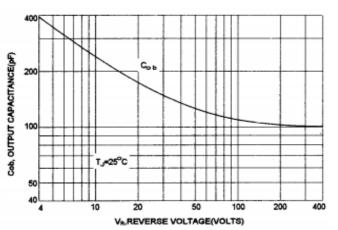
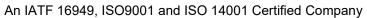


Fig 6: Output Capacitance



MJ10022 MJ10023 Rev0_ 28072020E









TYPICAL CHARACTERISTICS CURVES

Fig 9: Active Region Safe Operating Area

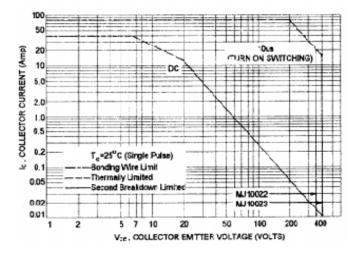
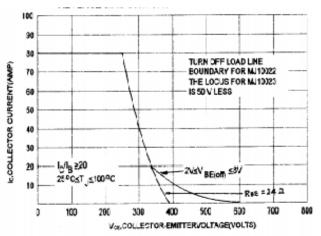


Fig 10: Reverse BIAS Switching Operating Area



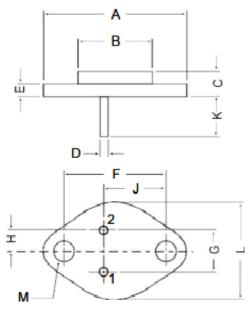


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PACKAGE DETAILS

TO-3 Metal Can Plastic package

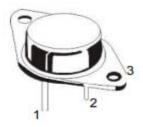


DIM	MIN.	MAX.	
Α	_	39.37	
В	_	22.22	
C	6.35	8.50	
D	0.96	1.09	
E	_	1.77	
F	29.90	30.40	
G	10.69	11.18	
Н	5.20	5.72	
J	16.64	17.15	
K	11.15	12.25	
L	_	26.67	
<u>M</u>	3.84	4.19	

Dimensions are in mm

PIN CONFIGURATION

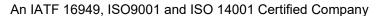
- 1. Base
- 2. Emitter
- 3. Collector



Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTERCARTONBOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	GrWt
TO-3	100 pcs/pkt	1.3 kg/100 pcs	12.5" x 8" x 1.8"	0.1K	17" x 11.5" x 21"	2K	27.5 kgs







Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- · Humidity between 40 to 70 %RH
- · Air should be clean.
- · Avoid harmful gas or dust.
- · Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- · Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- · Mechanical stress such as vibration and impact shall be avoided.
- · The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level					
Level	Time	Condition			
1	Unlimited	≤30 °C / 85% RH			
2	1 Year	≤30 °C / 60% RH			
2a	4 Weeks	≤30 °C / 60% RH			
3	168 Hours	≤30 °C / 60% RH			
4	72 Hours	≤30 °C / 60% RH			
5	48 Hours	≤30 °C / 60% RH			
5a	24 Hours	≤30 °C / 60% RH			
6	Time on Label(TOL)	≤30 °C / 60% RH			



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Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/s

support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s). CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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MJ10022 MJ10023 Rev0 28072020E