



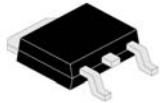
Continental Device India Pvt. Limited

An IATF 16949, ISO9001 and ISO 14001 Certified Company



3-Terminal 1A Positive Voltage Regulator

CL7812DT



TO-252 (DPAK)

**TO-252 (DPAK)
Surface Mount Package
RoHS compliant**

GENERAL DESCRIPTION:

The CL7812DT series of three terminal positive regulators are available in the TO-252(DPAK) package and with several fixed output voltages, making them useful in a wide range of applications. Each type employs internal current limiting, thermal shut down and safe operating area protection, making it essentially indestructible. If adequate heat sinking is provided, they can deliver over 1.0A output current. Although designed primarily as fixed voltage regulators, these devices can be used with external components to obtain adjustable voltages and currents.

ABSOLUTE MAXIMUM RATINGS ($T_a = 25\text{ }^\circ\text{C}$)

Parameter	Symbol	Typ	Unit
Input Voltage	V_I	19	V
Output Voltage	V_O	12	V
Peak Current	I_{PK}	1.7	A
Operating Temperature Range	T_{OPR}	0~125	$^\circ\text{C}$
Storage Temperature Rang	T_{STG}	-65~150	$^\circ\text{C}$

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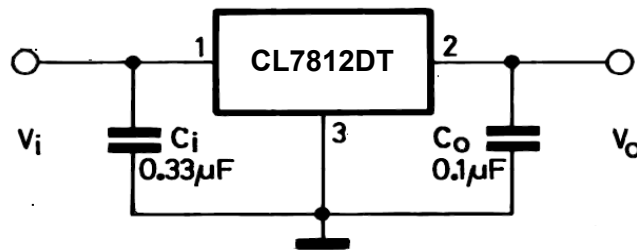


ELECTRICAL CHARACTERISTICS ($T_a = 25\text{ }^\circ\text{C}$)

(Refer to test circuit, $I_o = 500\text{mA}$, $V_i = 19\text{V}$, $C_i = 0.33\mu\text{F}$, $C_o = 0.1\mu\text{F}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	V_o	$T_j = 25\text{ }^\circ\text{C}$, $V_i = 15\text{V} - 27\text{V}$, $I_o = 5.0\text{ mA} - 1.0\text{A}$	11.5	12	12.5	V
Line Regulation	ΔV_o	$T_j = 25\text{ }^\circ\text{C}$, $V_i = 15\text{V} - 30\text{V}$	-	-	240	mV
		$T_j = 25\text{ }^\circ\text{C}$, $V_i = 16\text{V} - 22\text{V}$	-	-	120	
Load Regulation	ΔV_o	$T_j = 25\text{ }^\circ\text{C}$, $I_o = 5.0\text{mA} - 1.0\text{A}$	-	-	240	mV
		$T_j = 25\text{ }^\circ\text{C}$, $I_o = 250\text{mA} - 750\text{mA}$	-	-	120	
Quiescent Current	I_q	$T_j = 25\text{ }^\circ\text{C}$	-	-	8	mA
Quiescent Current Change	ΔI_q	$I_o = 5.0\text{mA} - 1.0\text{A}$	-	-	0.5	mA
		$T_j = 25\text{ }^\circ\text{C}$, $V_i = 15\text{V} - 25\text{V}$	-	-	1	mA
Output Voltage Drift	$\Delta V_o / \Delta T$	$I_o = 5.0\text{mA}$	-	-1	-	mV/°C
Ripple Rejection	R_R	$f = 120\text{Hz}$, $V_o = 15\text{V} - 25\text{V}$	56	80	-	dB
Dropout Voltage	V_{Drop}	$I_o = 1.0\text{A}$, $T_j = 25\text{ }^\circ\text{C}$	-	2	-	V
Output Resistance	R_o	$f = 1\text{KHz}$	-	0.02	-	Ohm
Short Circuit Current	I_{SC}	$V_i = 35\text{V}$, $T_A = 25\text{ }^\circ\text{C}$	-	300	-	mA
Peak Current	I_{PK}	$T_j = 25\text{ }^\circ\text{C}$	-	-	1.7	A

Test Circuit



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Recommended Reflow Solder Profiles

The recommended reflow solder profiles for Pb and Pb-free devices are shown below.

Figure 1 shows the recommended solder profile for devices that have Pb-free terminal plating, and where a Pb-free solder is used.

Figure 2 shows the recommended solder profile for devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with a leaded solder.

Figure 1

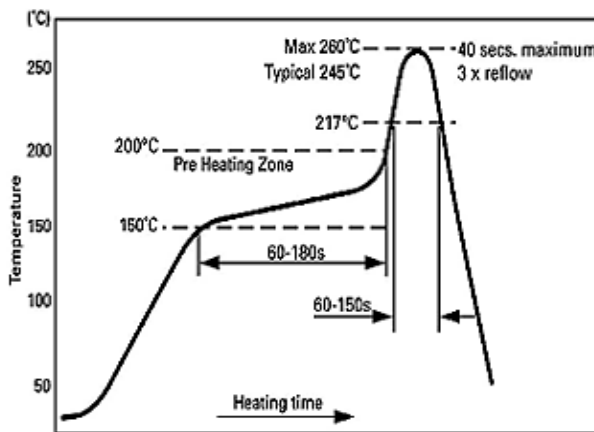
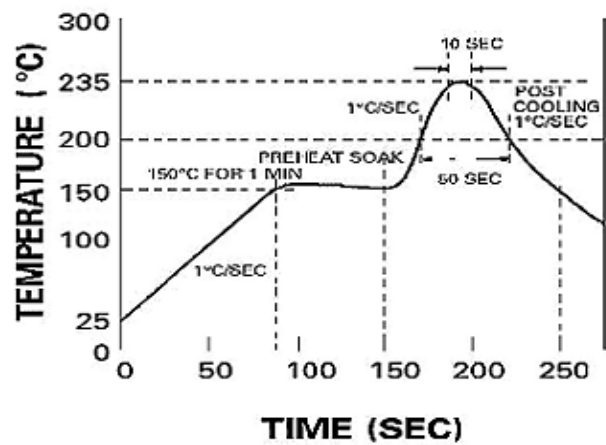


Figure 2

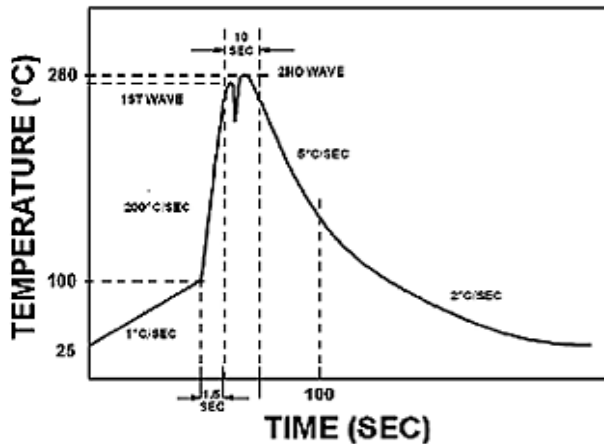


Reflow profiles in tabular form

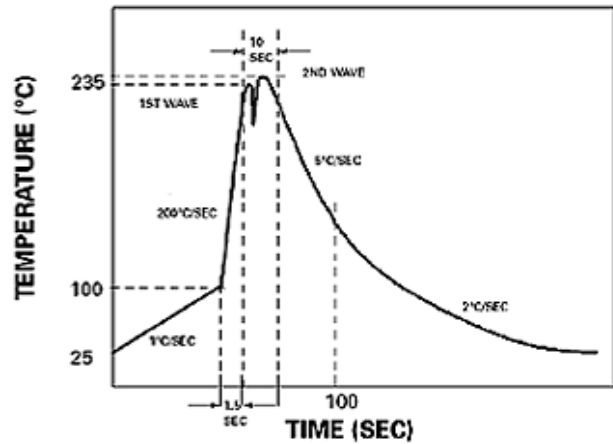
Profile Feature	Sn-Pb System	Pb-Free System
Average Ramp-Up Rate	~3°C/second	~3°C/second
Preheat		
– Temperature Range	150-170°C	150-200°C
– Time	60-180 seconds	60-180 seconds
Time maintained above:		
– Temperature	200°C	217°C
– Time	30-50 seconds	60-150 seconds
Peak Temperature	235°C	260°C max.
Time within +0 -5°C of actual Peak	10 seconds	40 seconds
Ramp-Down Rate	3°C/second max.	6°C/second max.

Recommended Wave Solder Profiles

The Recommended solder Profile For Devices with Pb-free terminal plating where a Pb-free solder is used



The Recommended solder Profile For Devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with leaded solder



Wave Profiles in Tabular Form

Profile Feature	Sn-Pb System	Pb-free System
Average Ramp-Up Rate	~200°C/second	~200°C/second
Heating rate during preheat	Typical 1-2, Max 4°C/sec	Typical 1-2, Max 4°C/Sec
Final preheat Temperature	Within 125°C of Solder Temp	Within 125°C of Solder Temp
Peak Temperature	235°C	260°C max.
Time within +0 -5°C of actual Peak	10 seconds	10 seconds
Ramp-Down Rate	5°C/second max.	5°C/second max.



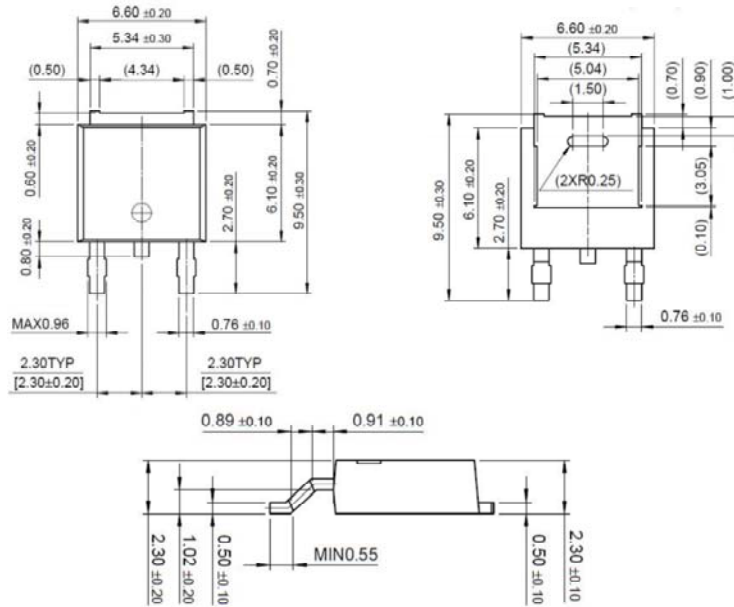
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PACKAGE DETAILS

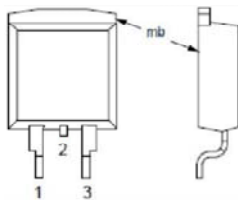
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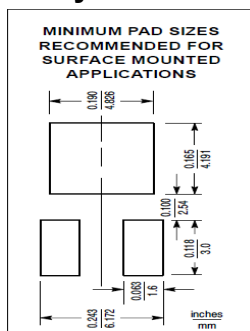
All dimensions are in mm

Pin configuration

- 1: IN
- 2: GND
- 3: OUT



PCB pads layout :



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Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level		
Level	Time	Condition
1	Unlimited	≤30 °C / 85% RH
2	1 Year	≤30 °C / 60% RH
2a	4 Weeks	≤30 °C / 60% RH
3	168 Hours	≤30 °C / 60% RH
4	72 Hours	≤30 °C / 60% RH
5	48 Hours	≤30 °C / 60% RH
5a	24 Hours	≤30 °C / 60% RH
6	Time on Label(TOL)	≤30 °C / 60% RH

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Customer Notes

Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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