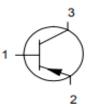




PNP SILICON PLANAR EPITAXIAL TRANSISTORS

BSR15 BSR16





SOT-23 SMD Package RoHS compliant

SOT-23

Marking

BSR15 = T7 BSR16 = T8

ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C Unless otherwise specified)

DADAMETER	OVMDOL				
PARAMETER	SYMBOL		BSR15	BSR16	UNIT
Collector-base voltage (open emitter)	-V _{CB0}	MAX	60	60	V
Collector-emitter voltage (open base)	-V _{CE0}	MAX	40	60	V
Emitter–base voltage (open collector)	-V _{EB0}	MAX	į	5	V
Collector current (d.c.)	-I _c	MAX	60	00	mA
Total power dissipation up to T _{amb} = 25 °C	P _{tot}	MAX	25	50	mW
Junction temperature	Тj	MAX	15	50	°C
D.C. current gain –I _c = 500mA; –V _{ce} = 10V	h _{FE}	MAX	30	50	
–off switching time –I _{Con} = 150mA; –I _{Bon} = I _{Boff} W= 15mA	t _{off}	MAX	10	00	ns
Transition frequency at f – 100 MHz –I _C = 50mA; –V _{CE} = 20V	f _T	MAX	20	00	MHz
Collector–base voltage (open emitter)	-V _{CB0}	MAX	60	60	V
Collector–emitter voltage (open base)	-V _{CE0}	MAX	40	60	V
Emitter–base voltage (open collector)	-V _{EB0}	MAX	5	5	V
Collector current (d.c.)	-I _c	MAX	60	00	mA
Power dissipation up to T _{amb} = 25 °C	P _{tot}	MAX	2	50	mW
Storage temperature	T _{stg}	MAX	-55 to	+150	°C
Junction temperature	T _i	MAX	1	50	°C

THERMAL RESISTANCE

From junction to ambient	R _{th j–a}	500	K/W	
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Continental Device India Pvt. Limited An IATF 16949, ISO9001 and ISO 14001 Certified Company

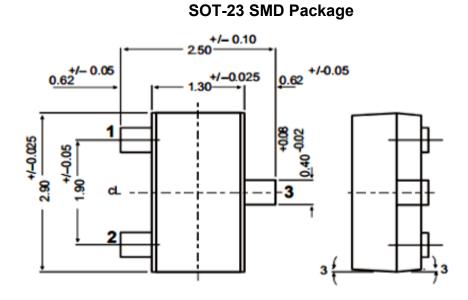
ELECTRICAL CHARACTERISTICS at (Ta = 25 °C Unless otherwise specified)

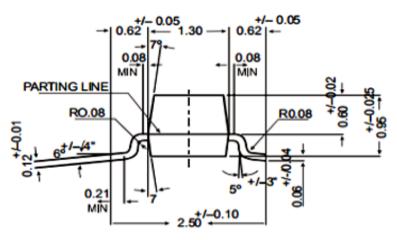
DADAMETED	OVMDOL			VALUE		
PARAMETER	SYMBOL TEST CONDITION			BSR15	BSR16	UNIT
	−I _{CB0}	I _E = 0; –V _{CB} = 50V	MIN	20	10	nA
Collector cut-off current	–І _{сво}	I _E = 0; -V _{CB} = 50V; T _i =150°C	MIN	20	10	μA
	$-I_{CEX}$	$-V_{EB} = 0,5V; -V_{CE} = 30V$	MIN	50		nA
Base current with reverse biased emitter junction	$-I_{BEX}$	-V _{EB} = 3V; -V _{CE} = 30V	MIN	50	0	nA
	–V _{CEsat}	L = 150m A; L = 15m A	MIN	0,4		V
Saturation voltages	$-V_{BEsat}$	–I _C = 150mA; –I _B = 15mA	MIN	1,3		V
Saturation voltages	–V _{CEsat}	L = 500 mA	MIN	1,6		V
	$-V_{BEsat}$	–I _C = 500mA; –I _B = 50mA	MIN	2,6		V
	h _{FE}	-I _C = 0,1mA; -V _{CE} = 10V	MAX	35	75	
	h _{FE}	–I _C = 1mA; –V _{CE} = 10V	MAX	50	100	
D.C. current gain	h _{FE}	–I _C = 10mA; –V _{CE} = 10V	MAX	75	100	
	h _{FE}	$-I_{\rm C} = 150$ mA; $-V_{\rm CE} = 10$ MAX 100 to 30		300		
	h _{FE}	–I _C = 500mA; –V _{CE} = 10V	MAX	30	50	
Transition frequency at f = 100 MHz	f _T	$-I_{C}$ = 50mA; $-V_{CE}$ = 20V; T_{amb} = 25°C	MAX	200		MHz





PACKAGE DETAILS





PIN CONFIGURATION (PNP)

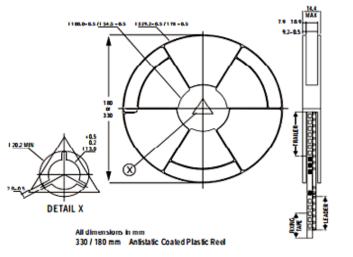
- 1 = BASE
- 2 = EMITTER
- 3 = COLLECTOR





PACKAGE DETAILS

SOT-23 Package Reel Information Reel specifications for Packing (13"/7" reels)



Size of Tape	8mm	8mm
Size of reel	330mm (13")	180mm (7")
No. of Device	10,000 Pcs	3,000 Pcs

NOTES:

1. The bandoier of 330mm reel contains at least 10,000 device.

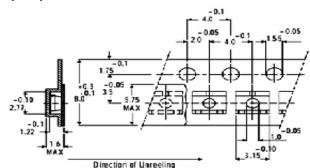
2. The bandoier of 180mm reel contains at least 3,000 device.

3. No more than 0.5% missing device/reel 50 empty compartments for 330mm reel. 15 empty compartments for 180mm reel.

4. Three consecutive empty places might be found provided this gap is followed by 6 consecutive devices.

5. The carrier tape (leader) starts with at least 75 empty positions (equivalent to 330 mm). In order to fix the carrier tape a self adhesive tape of 20 to 50 mm is applied. At the end of the bandolier at least 40 empty positions (equivalent to 160 mm) are there.

Tape Specification for SOT-23 Surface Mount Device



PACKAGE	STANDA	NRD PACK	INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Oty	Size	Oty	Size	Oty	Gr Wt
SOT-23 T&R	3K/reel	136 gm/3K pcs	3" x 7.5" x 7.5" 9" x 9" x 9"		17" x 15" x 13.5" 19" x 19" x 19"	192.0K 408.0K	12 kgs 28 kgs
	10K/reel	415 gm/10K pc s	13" x 13" x 0.5"	10.0K	17" x 15" x 13.5"	300.0K	16 kgs

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Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- · Humidity between 40 to 70 %RH
- · Air should be clean.
- · Avoid harmful gas or dust.
- \cdot Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- · Mechanical stress such as vibration and impact shall be avoided.
- · The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start. For this, the following JEDEC table may be referred:

JEDEC MSL Level				
Level	Time	Condition		
1	Unlimited	≤30 °C / 85% RH		
2	1 Year	≤30 °C / 60% RH		
2a	4 Weeks	≤30 °C / 60% RH		
3	168 Hours	≤30 °C / 60% RH		
4	72 Hours	≤30 °C / 60% RH		
5	48 Hours	≤30 °C / 60% RH		
5a	24 Hours	≤30 °C / 60% RH		
6	Time on Label(TOL)	≤30 °C / 60% RH		





Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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