

SILICON POWER SWITCHING TRANSISTORS

2N5320 2N5321 NPN
2N5322 2N5323 PNP



TO-39

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Metal Can Package
RoHS compliant

APPLICATION:

Medium Power Amplifier and Switching Applications

ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL	2N5320	2N5321	2N5322	2N5323	UNIT
Collector Emitter Voltage	V_{CEO}	75	50	75	50	V
Collector Base Voltage	V_{CBO}	100	75	100	75	V
Emitter Base Voltage	V_{EBO}	7	5	7	5	V
Collector Current - Continuous	I_C	2.1				A
Base Current	I_B	1.0				A
Power Dissipation@ Ta=25°C Derate Above 25°C	P_D	1 5.71				W mW/°C
Power Dissipation@ Tc=25°C Derate Above 25°C	P_D	10 57.14				W mW/°C
Operating And Storage Junction Temperature Range	T_j, T_{stg}	-65 to +200				°C

THERMAL CHARACTERISTICS

Junction to Ambient in free air	$R_{th(j-a)}$	175	°C/W
Junction to Case	$R_{th(j-c)}$	17.5	°C/W

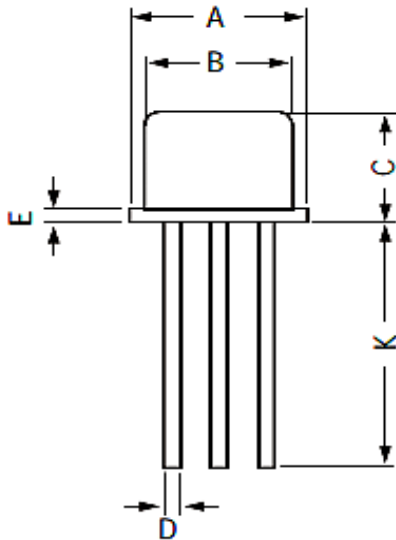
ELECTRICAL CHARACTERISTICS at (Ta = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Collector Emitter Voltage	V_{CEO}	$I_C=100mA, I_B=0$ 2N5320/5322	75	--	V
		2N5321/5323	50	--	V
Collector Cut Off Current	I_{CEX}	$V_{CE}=70V, V_{BE}=1.5V, T_c=150^\circ C$ 2N5320/5322	--	5	mA
		$V_{CE}=45V, V_{BE}=1.5V, T_c=150^\circ C$ 2N5321/5323	--	5	mA
		$V_{CE}=100V, V_{BE}=1.5V$ 2N5320/5322	--	100	μA
		$V_{CE}=75V, V_{BE}=1.5V$ 2N5321/5323	--	100	μA
Emitter Cut Off Current	I_{EBO}	$V_{BE}=5V, I_C=0$ 2N5321/5323	--	100	μA
		$V_{BE}=7V, I_C=0$ 2N5320/5322	--	100	μA
DC Current Gain	h_{FE}^1	$I_C=1A, V_{CE}=2V$ 2N5320/5322	10	--	
		$I_C=0.5A, V_{CE}=4V$ 2N5320/5322 2N5321/5323	30 40	160 250	
Collector Emitter Saturation Voltage	$V_{CE(sat)}^1$	$I_C=500mA, I_B=50mA$ 2N5320	--	0.5	V
		2N5321	--	0.8	V
		2N5322	--	0.7	V
		2N5323	--	1.2	V
Base Emitter On Voltage	$V_{BE(on)}^1$	$I_C=500mA, V_{CE}=4V$ 2N5320/5322	--	1.1	V
		2N5321/5323	--	1.4	V
DYNAMIC CHARACTERISTICS					
Small Signal Current Gain	h_{fe}	$I_C=50mA, V_{CE}=4V, f=10MHz$	5		
SWITCHING CHARACTERISTICS					
Turn On time	t_{on}	$V_{CC}=30V, I_C=500mA, I_{B1}=50mA$ 2N5320/5321 2N5322/5323	--	80 100	ns
Turn Off time	t_{off}	$V_{CC}=30V, I_C=500mA, I_{B1}=I_{B2}=50mA$ 2N5320/5321 2N5322/5323	--	800 1000	ns

Note:

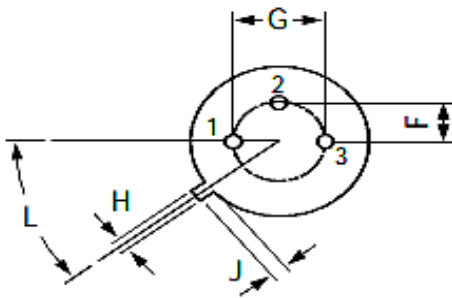
1. Pulsed: Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$

PACKAGE DETAILS



DIM	MIN	MAX
A	8.50	9.39
B	7.74	8.50
C	6.09	6.60
D	0.40	0.53
E	--	0.88
F	2.41	2.66
G	4.82	5.33
H	0.71	0.86
J	0.73	1.02
K	12.70	--
L	42 DEG	48 DEG

All Dimensions are in mm



Pin Configuration

1. Emitter
2. Base
3. Collector





Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level		
Level	Time	Condition
1	Unlimited	≤30 °C / 85% RH
2	1 Year	≤30 °C / 60% RH
2a	4 Weeks	≤30 °C / 60% RH
3	168 Hours	≤30 °C / 60% RH
4	72 Hours	≤30 °C / 60% RH
5	48 Hours	≤30 °C / 60% RH
5a	24 Hours	≤30 °C / 60% RH
6	Time on Label(TOL)	≤30 °C / 60% RH



Continental Device India Pvt. Limited

An IATF 16949, ISO9001 and ISO 14001 Certified Company



Customer Notes

Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered trademark of

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