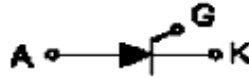


## SENSITIVE GATE SCR

2N5064



TO-92 Leaded  
Plastic Package  
RoHS compliant

TO-92

### FEATURES:

Designed to be Interfaced Directly to Microcontrollers, Logic Integrated Circuits and other Low Power Trigger Circuits

### APPLICATION:

General Purpose Switching and Phase Control Applications

### ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C Unless otherwise specified)

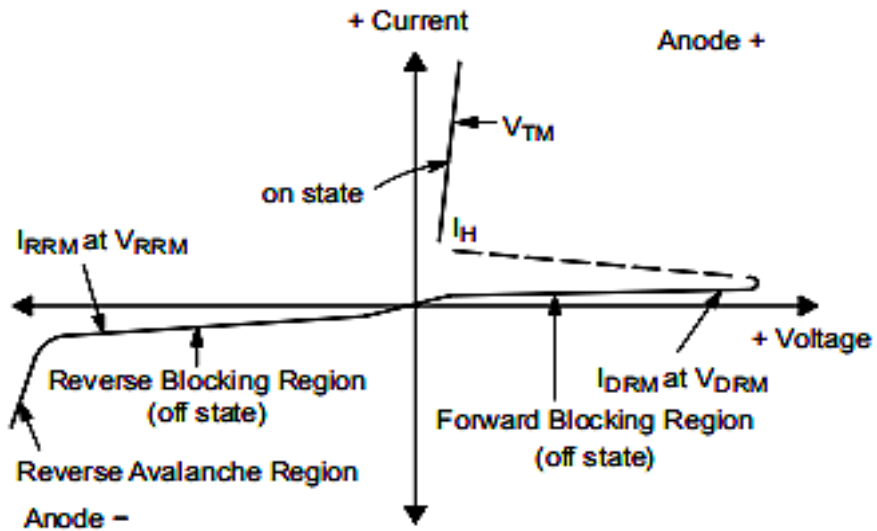
PARAMETER	SYMBOL	VALUE	UNIT
Peak Repetitive Off State Voltages	*V <sub>DRM</sub>	200	V
	*V <sub>RRM</sub>	200	V
Forward Current RMS	I <sub>T(RMS)</sub>	0.8	A
Junction Temperature Range	T <sub>j</sub>	-65 to +125	°C
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C

### ELECTRICAL CHARACTERISTICS at (Ta = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	VALUE			UNIT
			MIN	TYP	MAX	
Peak Repetitive Forward or Reverse Blocking Voltage	V <sub>DRM</sub> , V <sub>RRM</sub>	I <sub>D</sub> = 10µA	200	--	--	V
Peak Repetitive Forward or Reverse Blocking Current	I <sub>DRM</sub> , I <sub>RRM</sub>	V <sub>D</sub> =Rated V <sub>DRM</sub> and V <sub>RRM</sub>	--	--	10	µA
Gate Trigger Current	I <sub>GT</sub>	V <sub>AK</sub> =7V	--	--	200	µA
Holding Current	I <sub>H</sub>	V <sub>AK</sub> =7V, I <sub>HL</sub> =20mA	--	--	5	mA
On State Voltage	V <sub>TM</sub>	I <sub>TM</sub> =1.2A	--	--	1.7	V
Gate Trigger Voltage	V <sub>GT</sub>	V <sub>AK</sub> = 7V	--	--	0.8	V

**Voltage Current Characteristic of SCR**

Symbol	Parameter
$V_{DRM}$	Peak Repetitive Off State Forward Voltage
$I_{DRM}$	Peak Forward Blocking Current
$V_{RRM}$	Peak Repetitive Off State Reverse Voltage
$I_{RRM}$	Peak Reverse Blocking Current
$V_{TM}$	Peak on State Voltage
$I_H$	Holding Current



## TYPICAL CHARACTERISTICS CURVES

Fig 1: Maximum Case Temperature

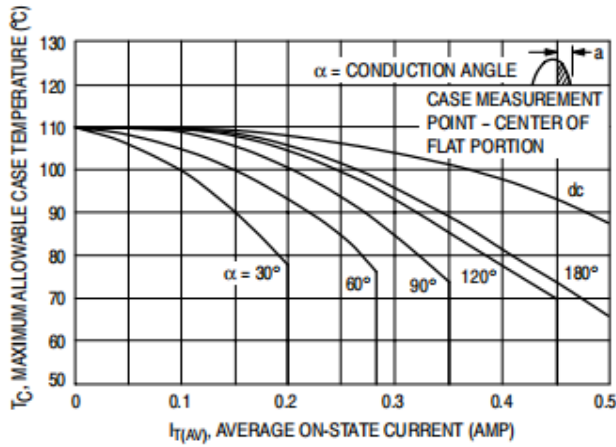


Fig 2: Maximum Ambient Temperature

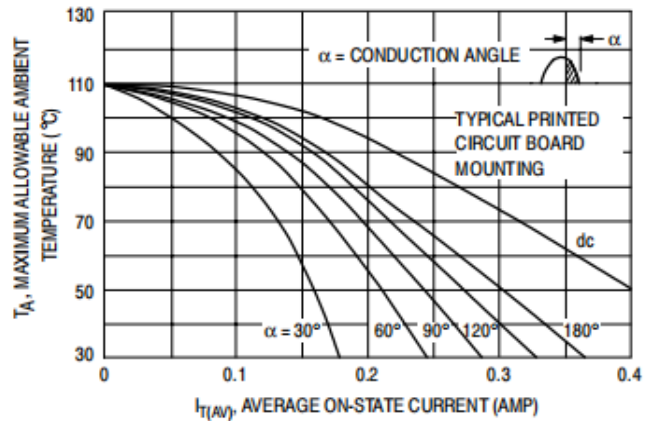


Fig 3: Typical Forward Voltage

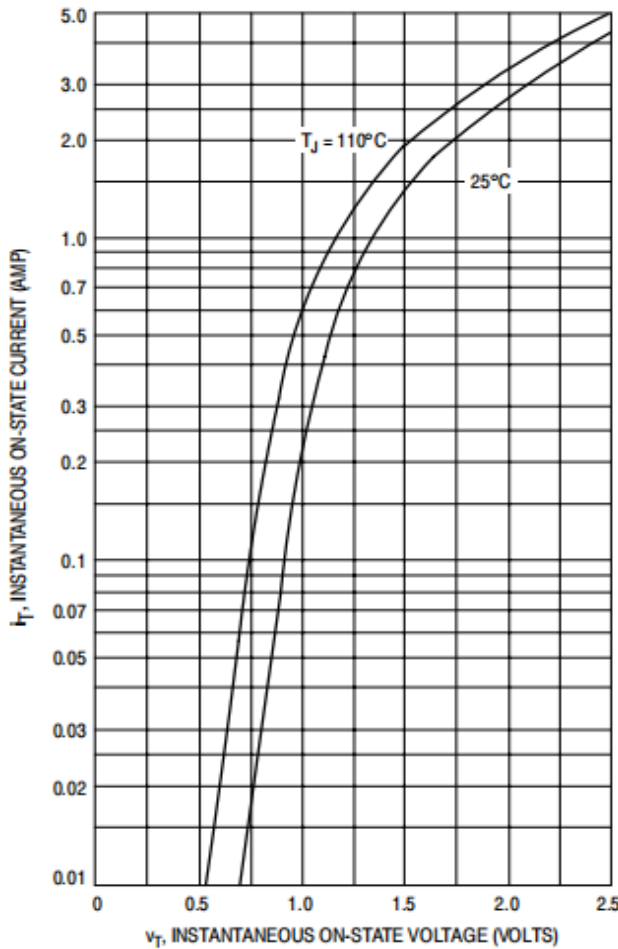


Fig 4: Maximum Non-Repetitive Surge Current

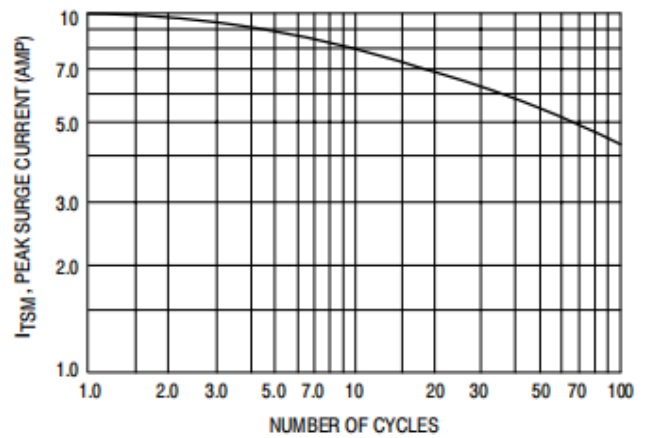
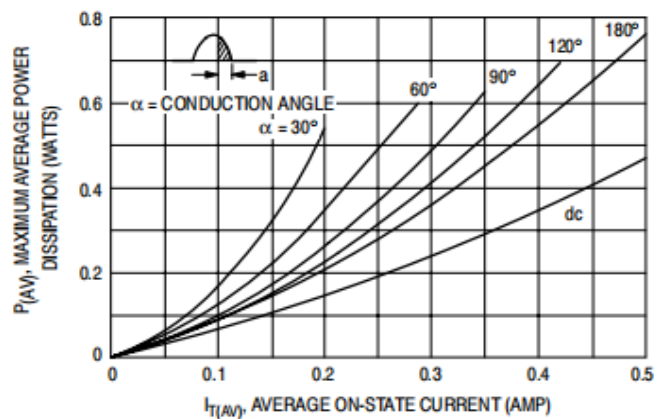


Fig 5: Power Dissipation



## TYPICAL CHARACTERISTICS CURVES

Fig 6: Thermal Response

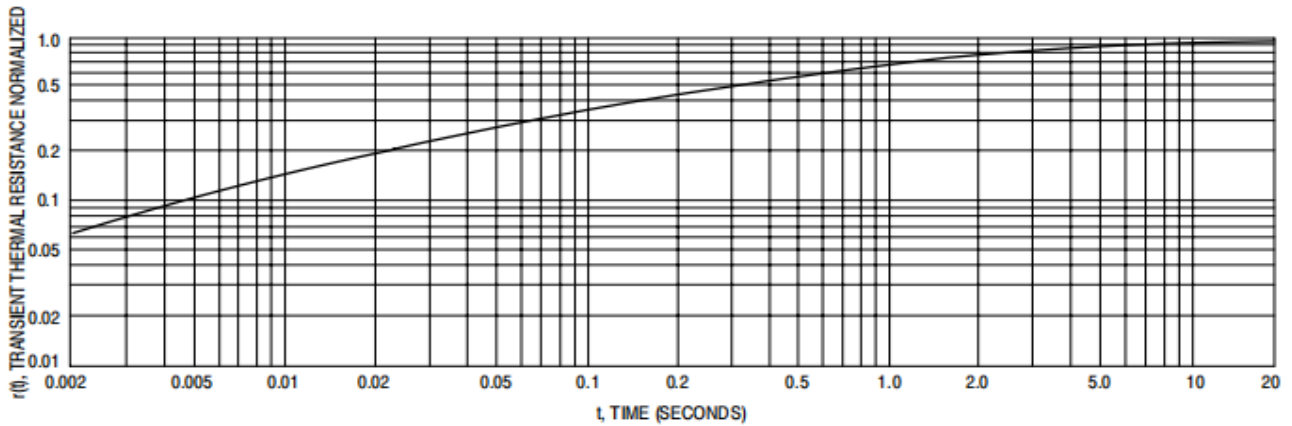


Fig 7: Typical Gate Trigger Voltage

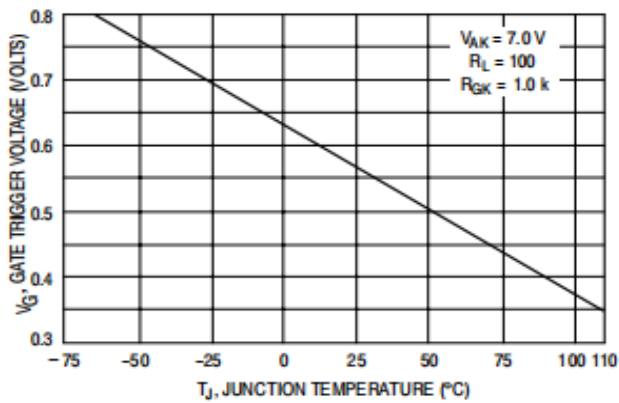


Fig 8: Typical Gate Trigger Current

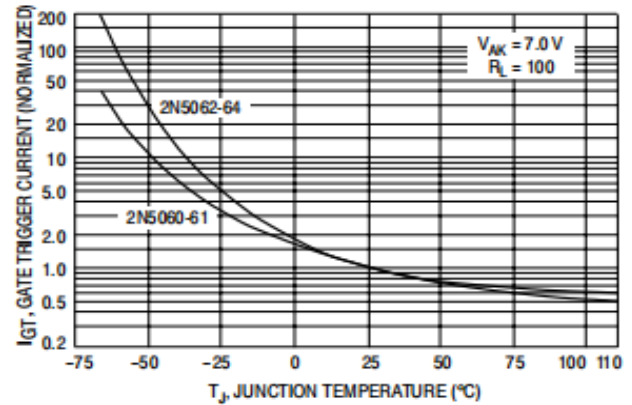
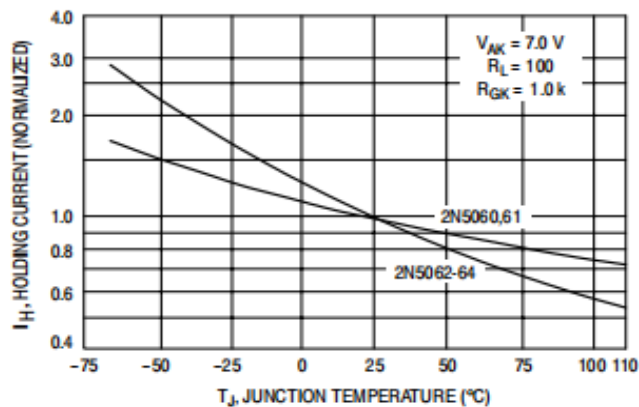
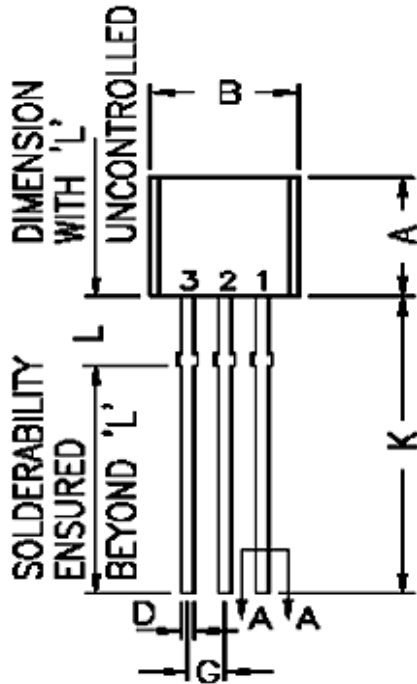


Fig 9: Typical Holding Current

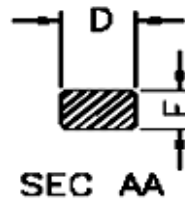
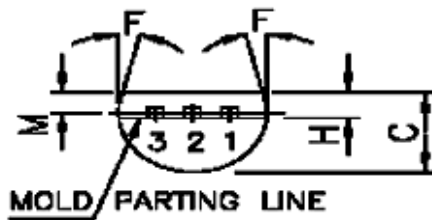


**PACKAGE DETAIL**

**TO-92 Plastic Package**

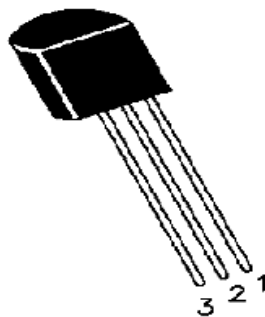


DIM	MIN	MAX
A	4,32	5,33
B	4,45	5,20
C	3,18	4,19
D	0,41	0,55
E	0,35	0,50
F	5 DEG	
G	1,14	1,40
H	1,20	1,40
K	12,70	--
L	1,982	2,082
M	1,03	1,20



**PIN CONFIGURATION**

- 1. ANODE
- 2. GATE
- 3. CATHODE





Continental Device India Pvt. Limited

An IATF 16949, ISO9001 and ISO 14001 Certified Company



### Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

#### **Shelf Life of CDIL Products**

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

#### **Floor Life of CDIL Products and MSL Level**

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level		
Level	Time	Condition
1	Unlimited	≤30 °C / 85% RH
2	1 Year	≤30 °C / 60% RH
2a	4 Weeks	≤30 °C / 60% RH
3	168 Hours	≤30 °C / 60% RH
4	72 Hours	≤30 °C / 60% RH
5	48 Hours	≤30 °C / 60% RH
5a	24 Hours	≤30 °C / 60% RH
6	Time on Label(TOL)	≤30 °C / 60% RH

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Rev01 15102020E



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## Customer Notes

### Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

### Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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CIN No. U32109DL1964PTC004291