

An IATF 16949, ISO9001 and ISO 14001 Certified Company

NPN SILICON PLANAR POWER TRANSISTORS

## TÜV SUD JATF 16949



2N3055

Metal Can Package RoHS compliant

ZNSUSS

**TO-3** 

15 AMPERES, 100 VOLTS,115 WATTS



TO-3

## APPLICATIONS:

General Purpose Switching and Amplifier Applications

## **ABSOLUTE MAXIMUM RATINGS** $(T_a = 25 \text{ }^{\circ}\text{C})$

DESCRIPTION	SYMBOL	VALUE	UNITS
Collector Base Voltage	V <sub>CBO</sub>	100	V
Collector Emitter Voltage	V <sub>CEO</sub>	60	V
Collector Emitter Voltage(R <sub>BE</sub> =100Ω)	V <sub>CER</sub>	70	V
Emitter Base Voltage	V <sub>EBO</sub>	7	V
Collector Current Continuous	l <sub>c</sub>	15	А
Base Current	I <sub>B</sub>	7	A
Power Dissipation @ T <sub>c</sub> =25°C	P <sub>tot</sub>	115	W
Derate Above 25°C		0.657	W/°C
Operating and Storage Junction	T <sub>j</sub> , T <sub>stg</sub>	- 65 to +200	°C
Temperature Range			

#### THERMAL RESISTANCE

Junction to Case	R <sub>th(j-c)</sub>	1.52	°C/W
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## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub>=25 ° C unless otherwise specified)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNITS
Collector Emitter Sustaing Voltage	V <sub>CEO(sus)</sub> *	I <sub>C</sub> =200mA, I <sub>B</sub> =0	60		٧
Collector Emitter Sustaing Voltage	V <sub>CER(sus)</sub> *	I <sub>C</sub> =200mA, R <sub>BE</sub> =100Ω	70		٧
Collector Cut off Current	I <sub>CEX</sub>	V <sub>CE</sub> =100V, V <sub>BE</sub> =(off)=1.5V		1.0	mA
		T <sub>c</sub> =150°C			
		V <sub>CE</sub> =100V, V <sub>BE</sub> =(off)=1.5V		5.0	
Collector Cut off Current	I <sub>CEO</sub>	V <sub>CE</sub> =30V, I <sub>B</sub> =0		0.7	mA
Emitter Cut off Current	I <sub>EBO</sub>	V <sub>BE</sub> =7V, I <sub>C</sub> =0		5.0	mA
Collector Emitter Saturation Voltage	V <sub>CE(Sat)</sub> *	I <sub>C</sub> =4A, I <sub>B</sub> =400mA		1.1	٧
		I <sub>C</sub> =10A, I <sub>B</sub> =3.3A		3.0	
Base Emitter on Voltage	V <sub>BE(on)</sub> *	I <sub>C</sub> =4A, V <sub>CE</sub> =4V		1.5	٧
DC Current Gain	h <sub>FE</sub> *	I <sub>C</sub> =4A, V <sub>CE</sub> =4V	20	80	
		I <sub>C</sub> =10A, V <sub>CE</sub> =4V	5		

## Second Breakdown

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNITS
Second Breakdown Collector Current	l <sub>s</sub> /b	V <sub>CE</sub> =40V,t=1.0 s,Nonrepetitive	2.87		А
with Base Forward Biased					

## **Dynamic Characteristics**

Current Gain - Bandwidth Product	f <sub>T</sub>	I <sub>C</sub> =0.5A, V <sub>CE</sub> =10V, f=1MHz	2.5		MHz
Small Signal Current Gain	h <sub>fe</sub>	I <sub>C</sub> =1A, V <sub>CE</sub> =4V, f=1KHz	15	120	
Small Signal Current Gain Cutoff	fh <sub>fe</sub>	I <sub>C</sub> =1A, V <sub>CE</sub> =4V, f=1KHz	10		KHz
Frequency					

<sup>\*</sup>Pulse Test: Pulse Width  $\leq$ 300 $\mu$ s, Duty Cycle  $\leq$ 2%

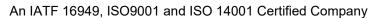
#### Note

Hfe groups are available in the band of 10.

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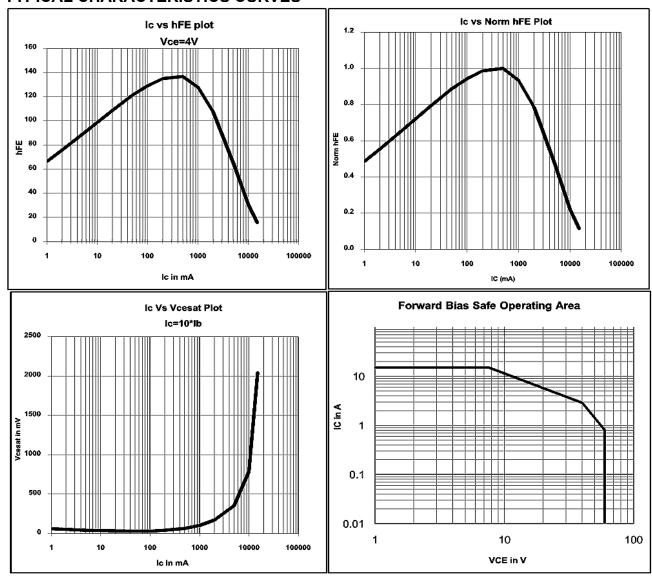




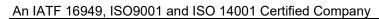




#### TYPICAL CHARACTERISTICS CURVES



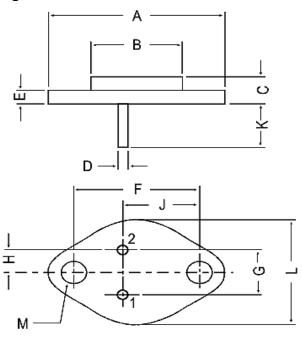






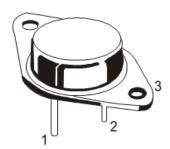


## **Package Details**



All dimensions in mm.

DIM	MIN.	MAX.
Α	_	39.37
В	_	22.22
С	6.35	8.50
D	0.96	1.09
Ε	_	1.77
F	29.90	30.40
G	10.69	11.18
Н	5.20	5.72
J	16.64	17.15
K	11.15	12.25
L	_	26.67
M	3.84	4.19



PIN CONFIGURATION

- 1. BASE
- 2. EMITTER
- 3. COLLECTOR

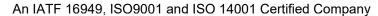
## **Packing Detail**

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-3	100 pcs/pkt	1.3 kg/100 pcs	12.5" x 8" x 1.8"	0.1K	17" x 11.5" x 21"	2K	27.5 kgs

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# Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- · Humidity between 40 to 70 %RH
- · Air should be clean.
- Avoid harmful gas or dust.
- · Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- · Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- · Mechanical stress such as vibration and impact shall be avoided.
- · The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

#### **Shelf Life of CDIL Products**

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

#### Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level						
Level	Time	Condition				
1	Unlimited	≤30 °C / 85% RH				
2	1 Year	≤30 °C / 60% RH				
2a	4 Weeks	≤30 °C / 60% RH				
3	168 Hours	≤30 °C / 60% RH				
4	72 Hours	≤30 °C / 60% RH				
5	48 Hours	≤30 °C / 60% RH				
5a	24 Hours	≤30 °C / 60% RH				
6	Time on Label(TOL)	≤30 °C / 60% RH				

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#### **Customer Notes**

#### **Component Disposal Instructions**

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

#### **Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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